

### REMARKS

The following remarks are being submitted as a full and complete response to the Office Action dated September 5, 2006. In view of the following remarks, the Examiner is respectfully requested to give due reconsideration to this application, to indicate the allowability of the claims, and to pass this case to issue.

### Status of the Claims

As outlined above, claims 11-15 stand for consideration in this application.

### Formal Rejections

Claims 11-15 were rejected under 35 U.S.C. §112, first paragraph, as failing to comply with the enablement requirement. Particularly, the Examiner asserted that the limitation recited in claim 11, “a data select circuit configured to select digital image data received from the first image signal input terminal at a timing of a voltage change from the first voltage to the second voltage as a first digital image data and at a timing of a voltage change from the second voltage to the first voltage of the internal clock signal as a second digital image data” is not shown in the specification or the drawings. Applicants respectfully disagree.

An exemplary circuit of “data select circuit” recited in claim 11 is a latch circuit shown in Figs. 10 and 11. The latch circuit has a flip-flop circuit 3 connected to one internal bus line B and a flip-flop circuit 1 connected to the other internal bus line A. As shown in Fig. 12, both of the flip-flop circuit 1 and the flip-flop circuit 3 receive an internal clock signal CLL2. The internal clock signal CLL2 is, for example, a signal with its duty ratio of 50 outputted from a clock compensation circuit 200 shown in Fig. 2 (page 15, lines 18-22 of the specification). Externally input display data is taken into the flip-flop circuit 1 at a timing of a rise-up in the clock signal CLL2, while externally input display data is accepted into the flip-flop circuit 3 at a timing of fall-down of the clock signal CLL2 (page 24, lines 18-21 of the specification). Both of the flip-flop circuit 1 and the flip-flop circuit 3 output the data to the internal bus line, as shown in Fig. 11.

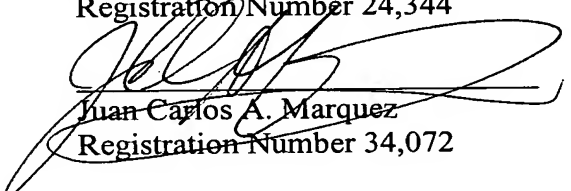
As set forth above, the specification and the drawings describe the features recited in claim 11, and therefore, claim 11 and its dependent claims 12-15 meet the requirements under 35 U.S.C. §112, first paragraph. Accordingly, withdrawal of this rejection is respectfully requested.

Conclusion

Favorable reconsideration of this application is respectfully solicited. Should there be any outstanding issues requiring discussion that would further the prosecution and allowance of the above-captioned application, the Examiner is invited to contact the Applicants' undersigned representative at the address and phone number indicated below.

Respectfully submitted,

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